

FIG. 1 PRIOR ART

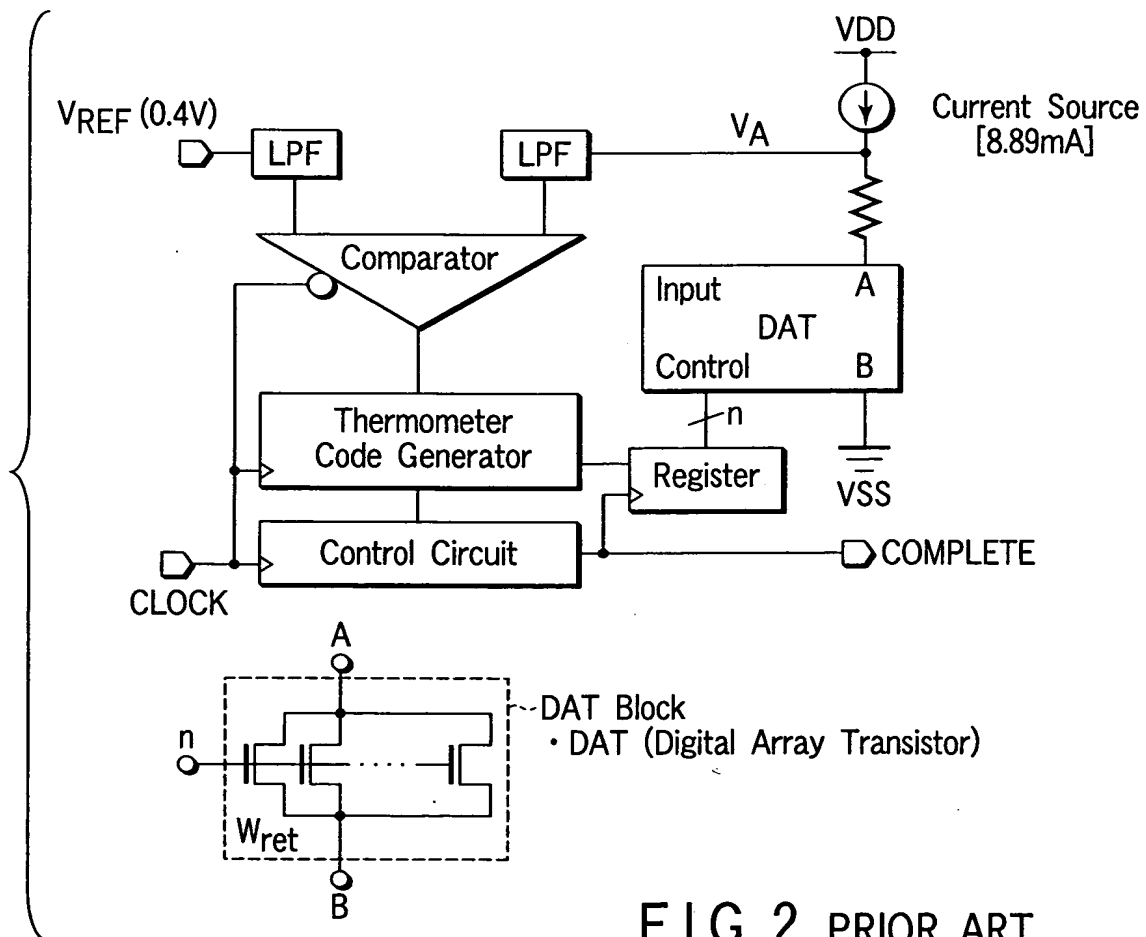


FIG. 2 PRIOR ART

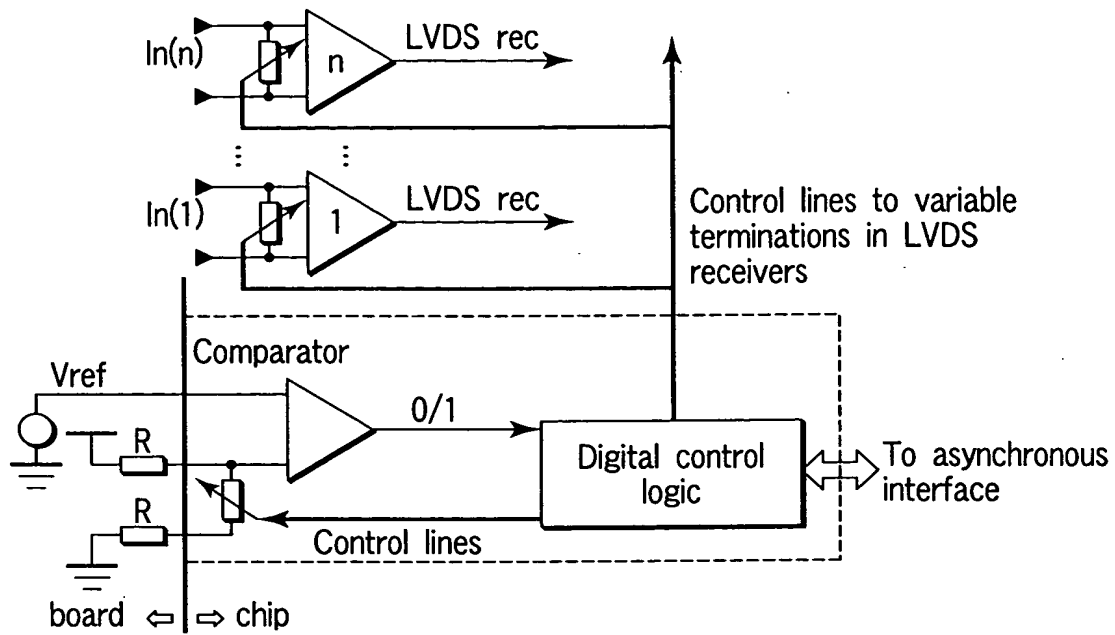


FIG. 3 PRIOR ART

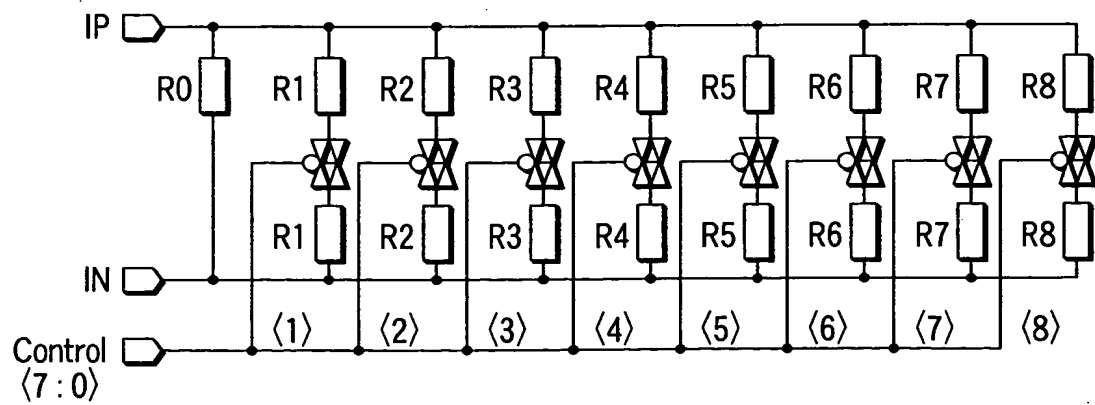


FIG. 4 PRIOR ART

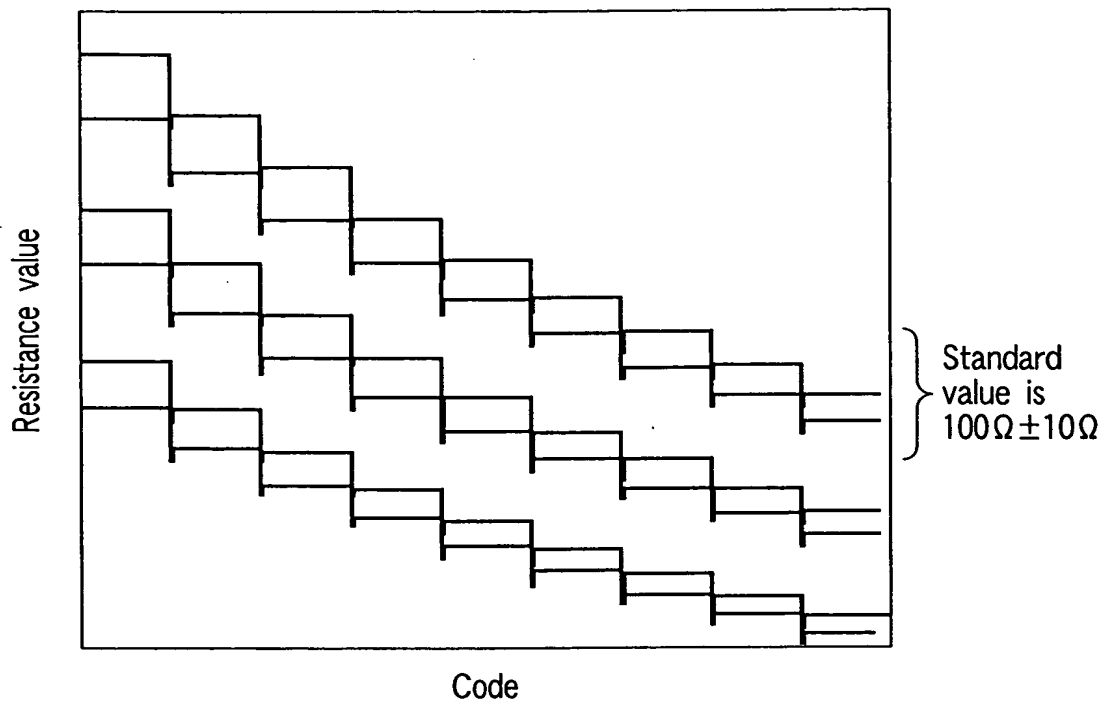


FIG. 5 PRIOR ART

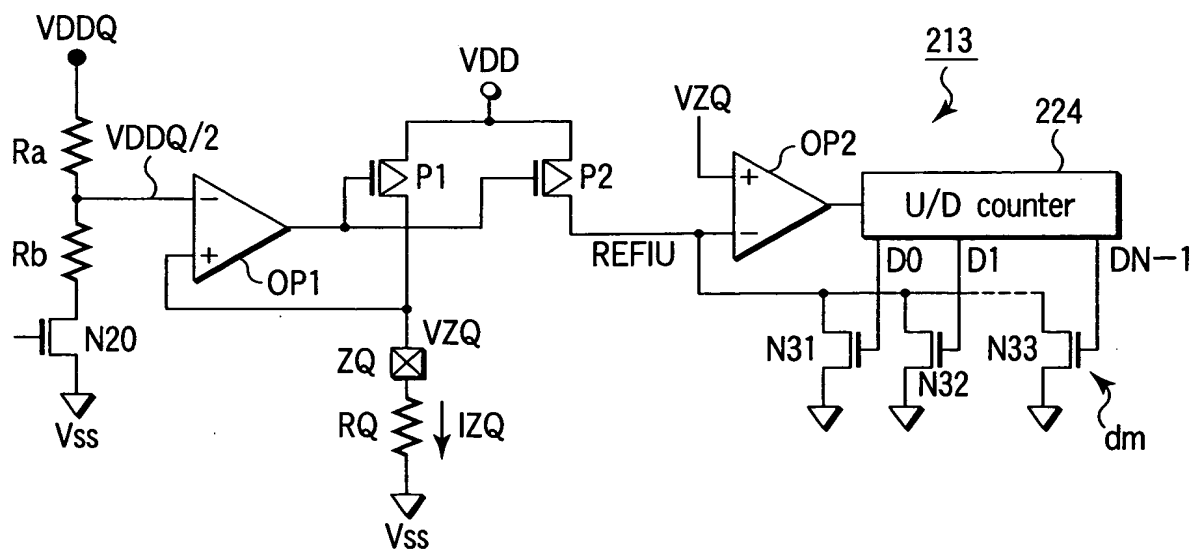


FIG. 6 PRIOR ART

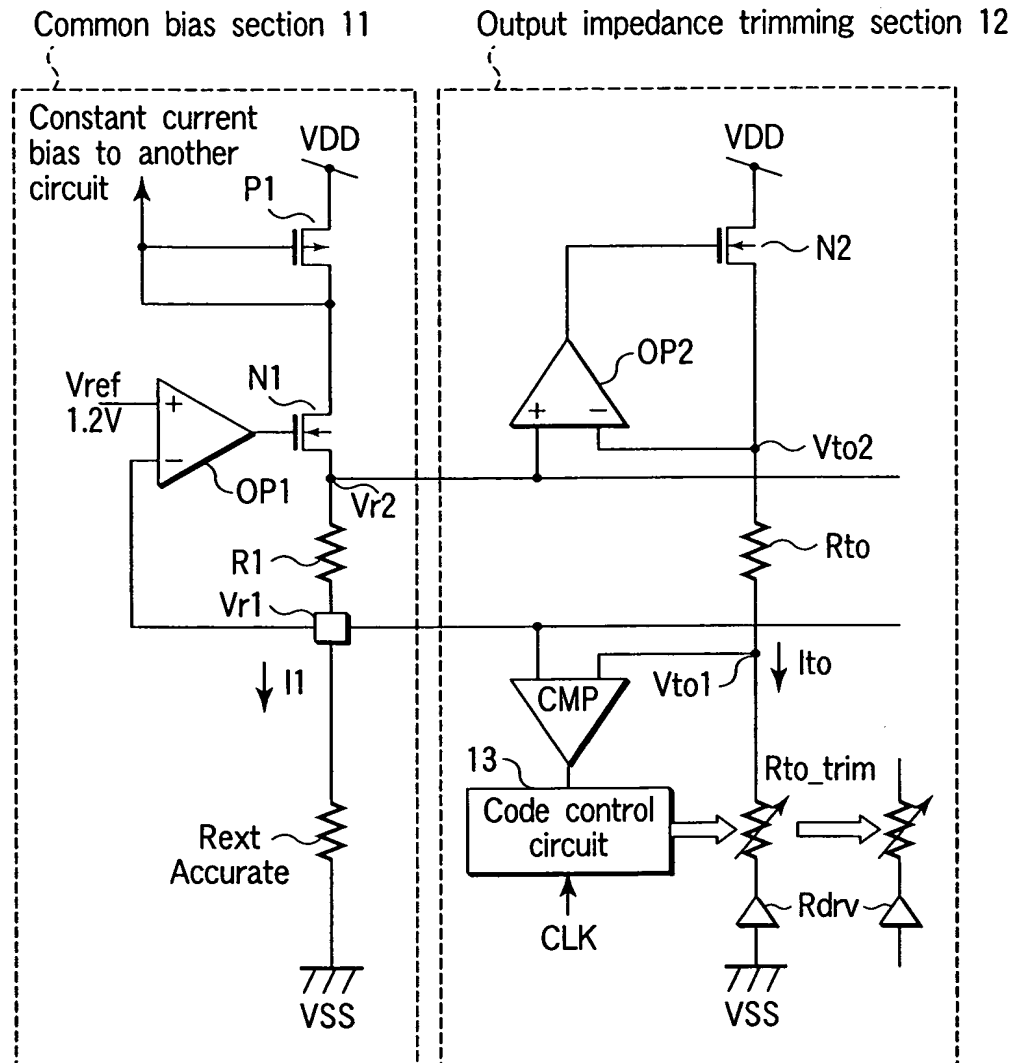


FIG. 7

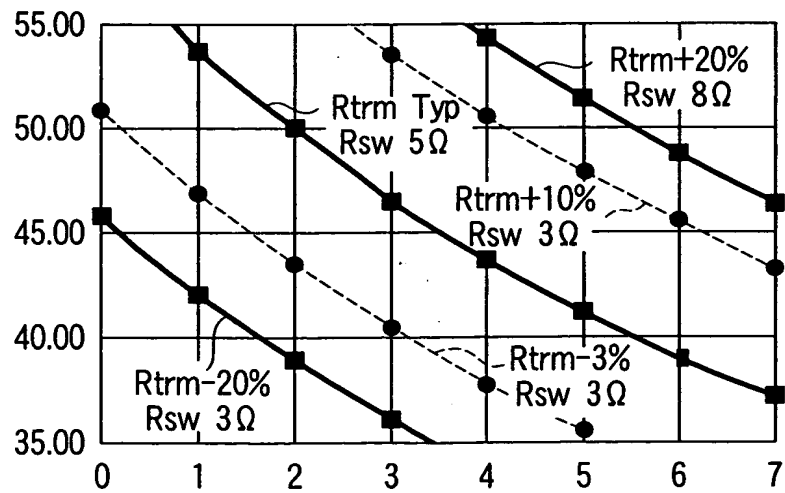


FIG. 8

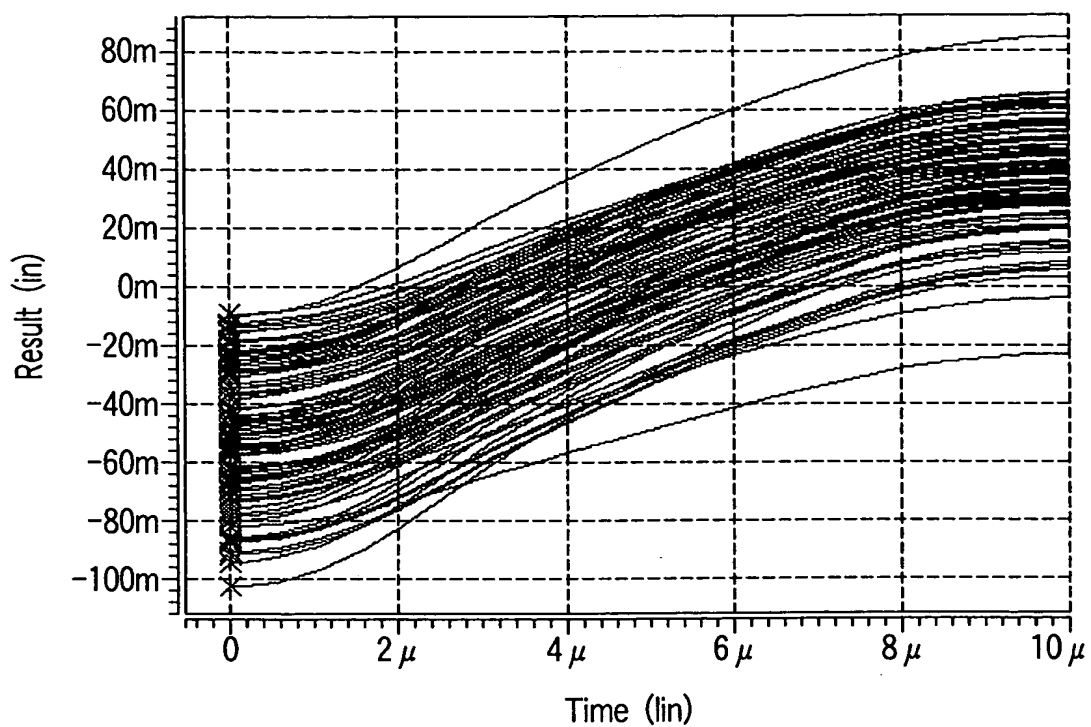


FIG. 9

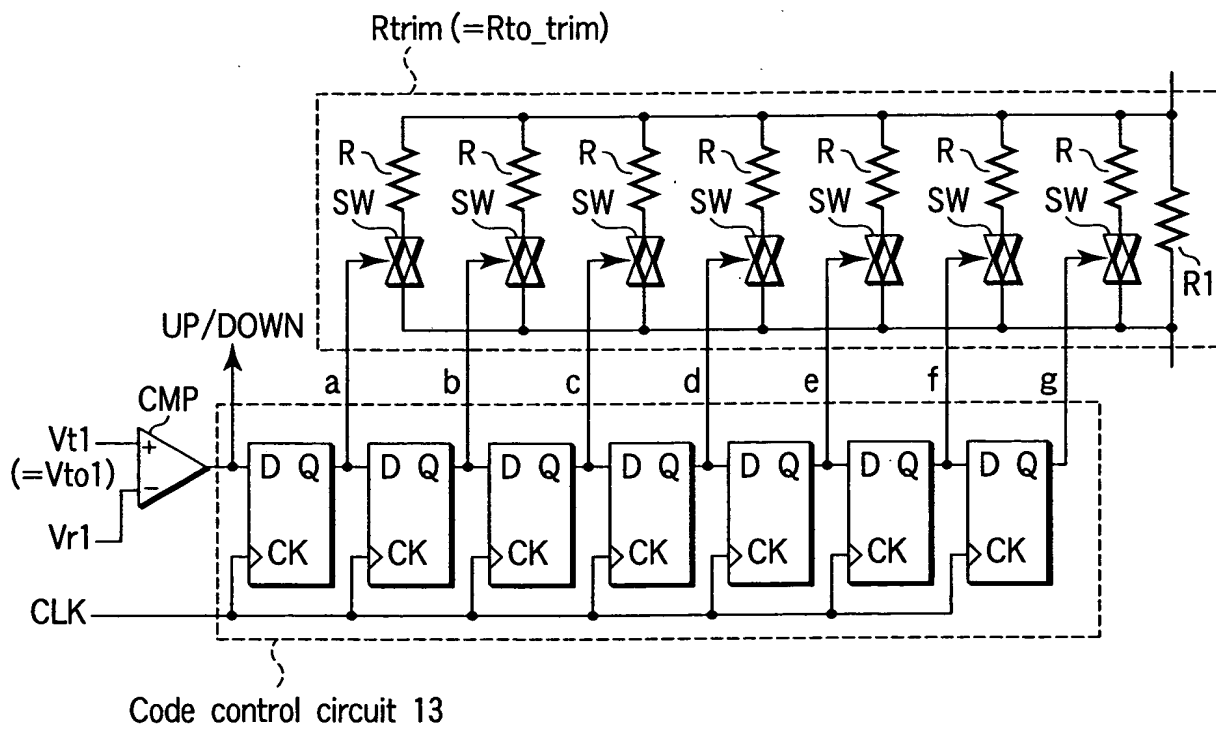


FIG. 10

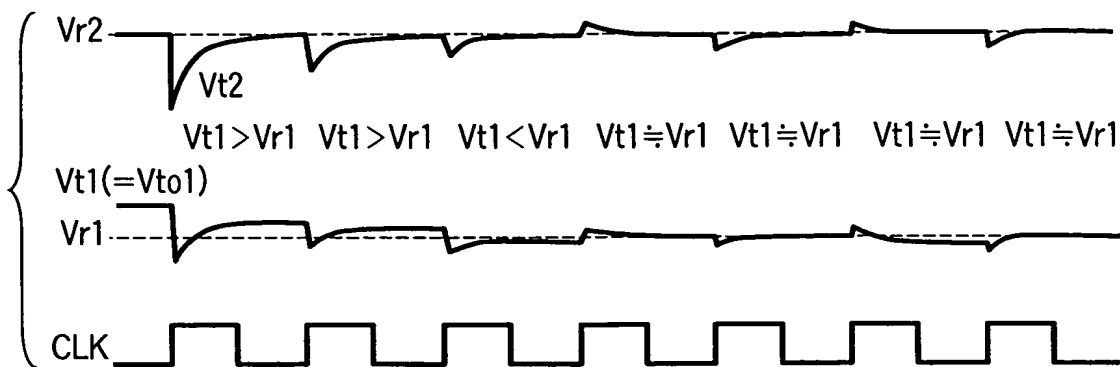


FIG. 11

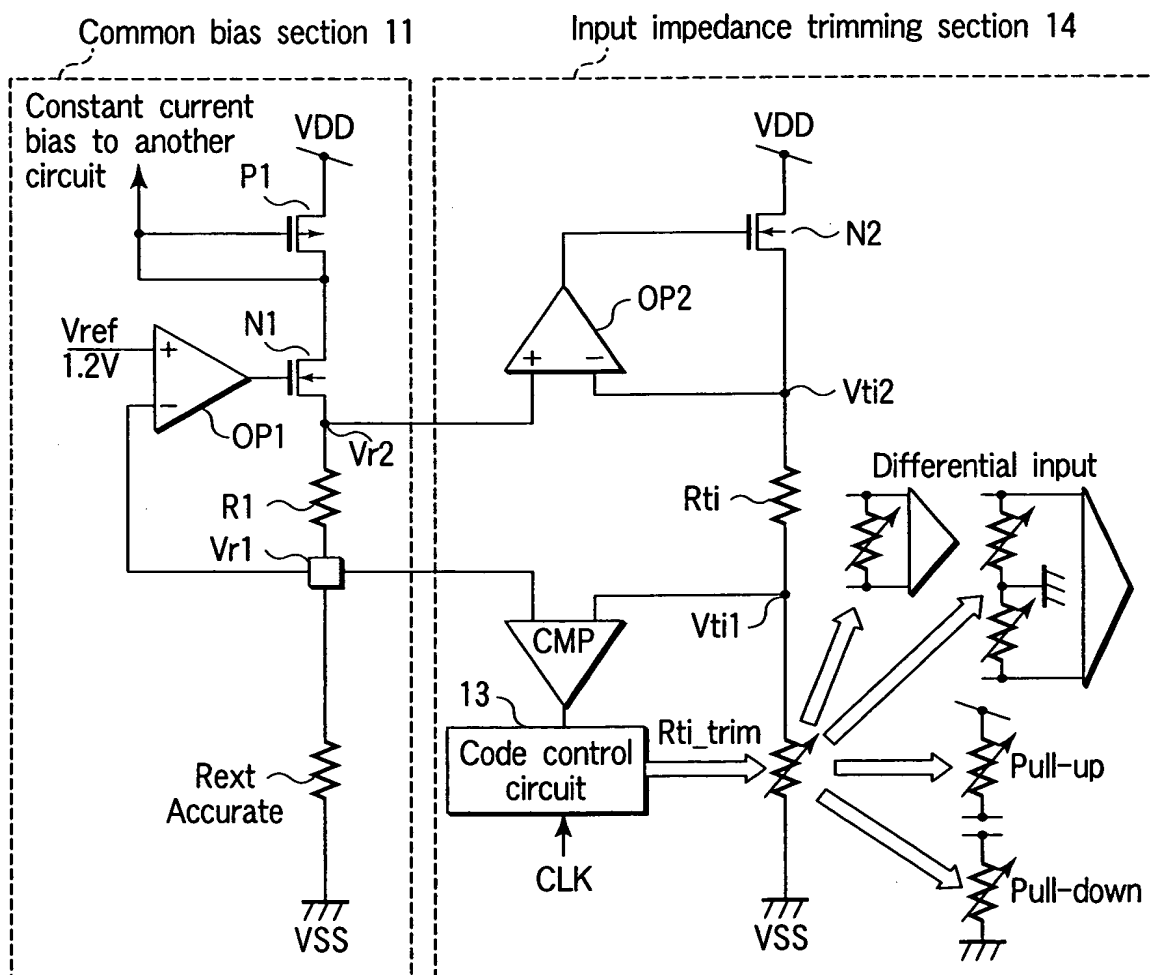


FIG. 12

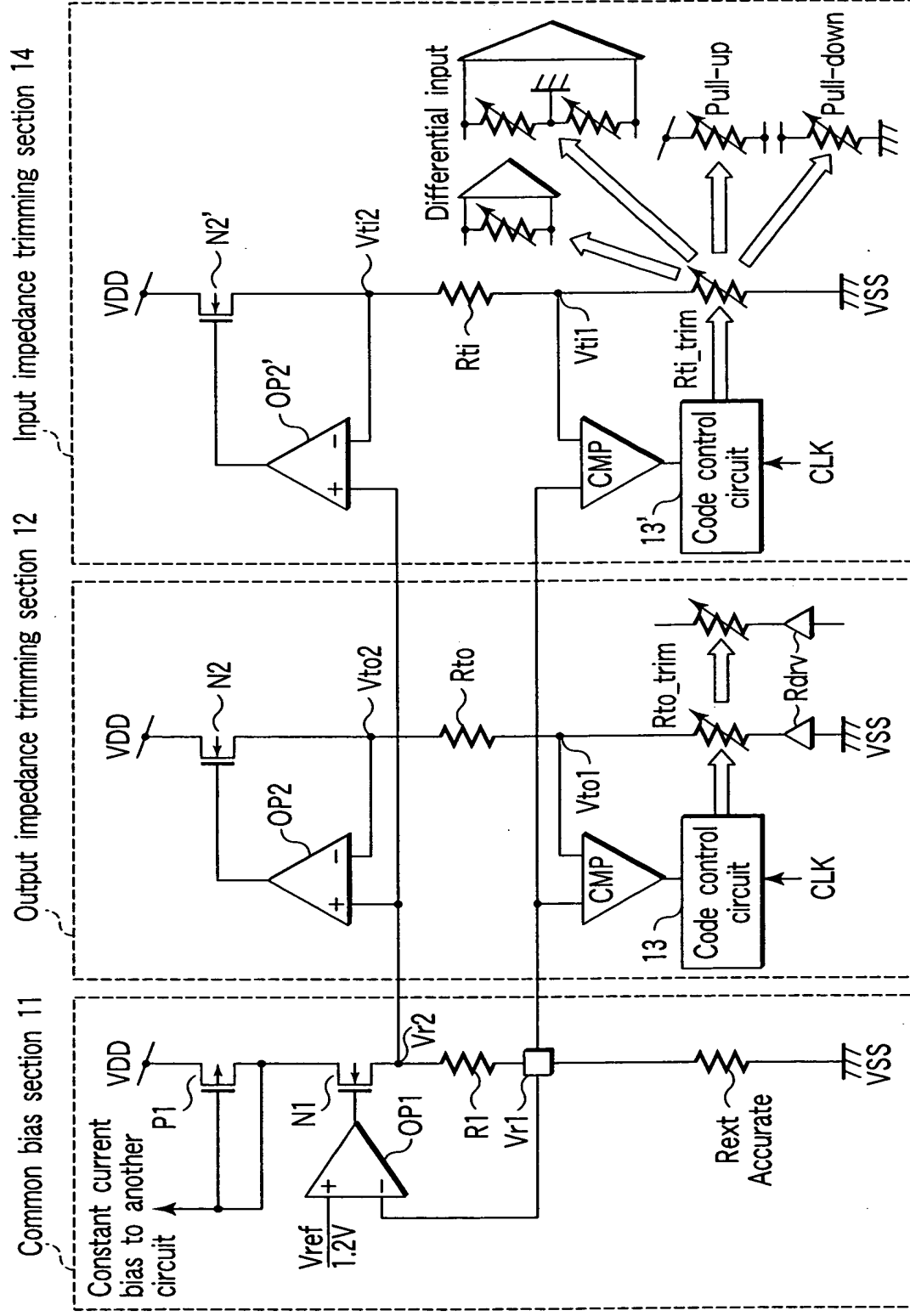


FIG. 13

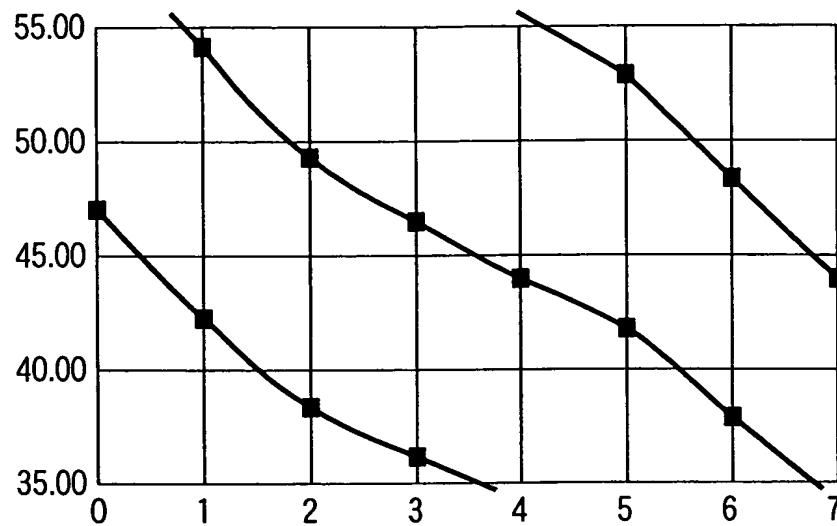


FIG. 14

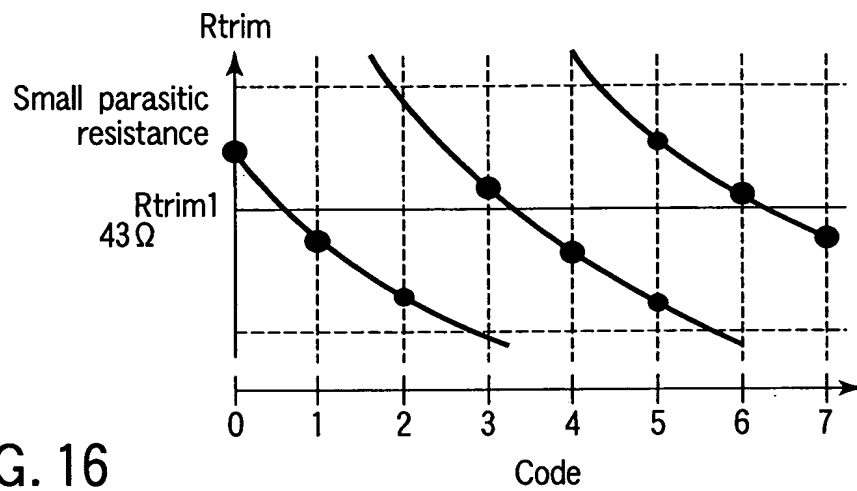


FIG. 16

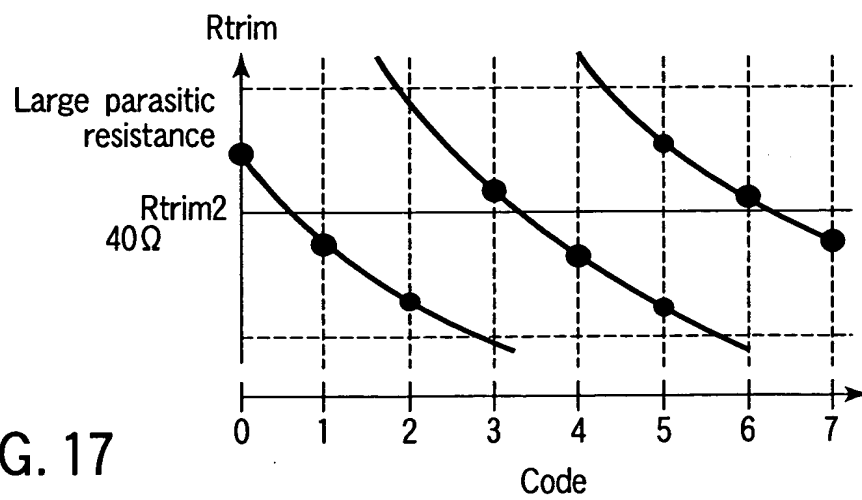


FIG. 17



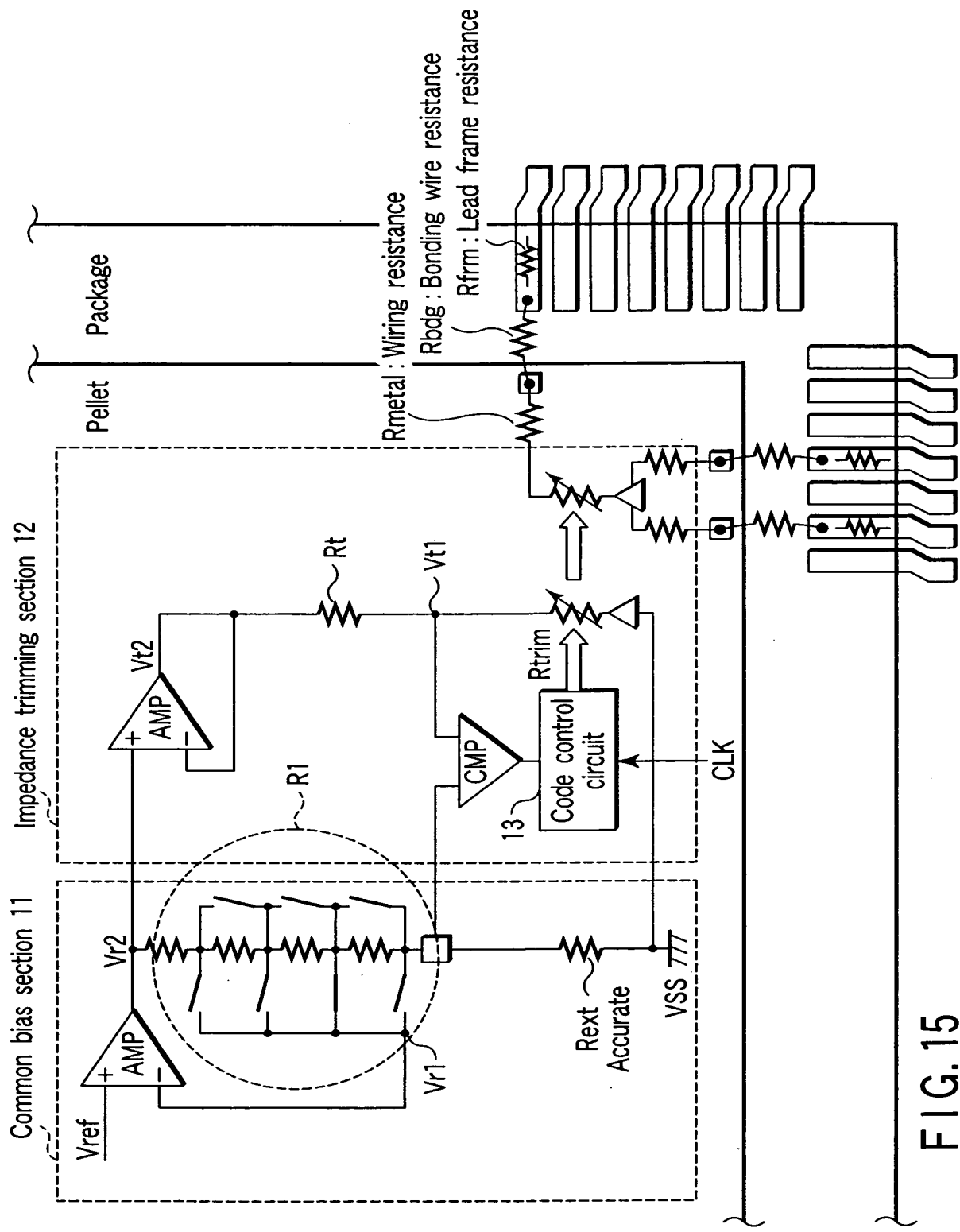


FIG. 15

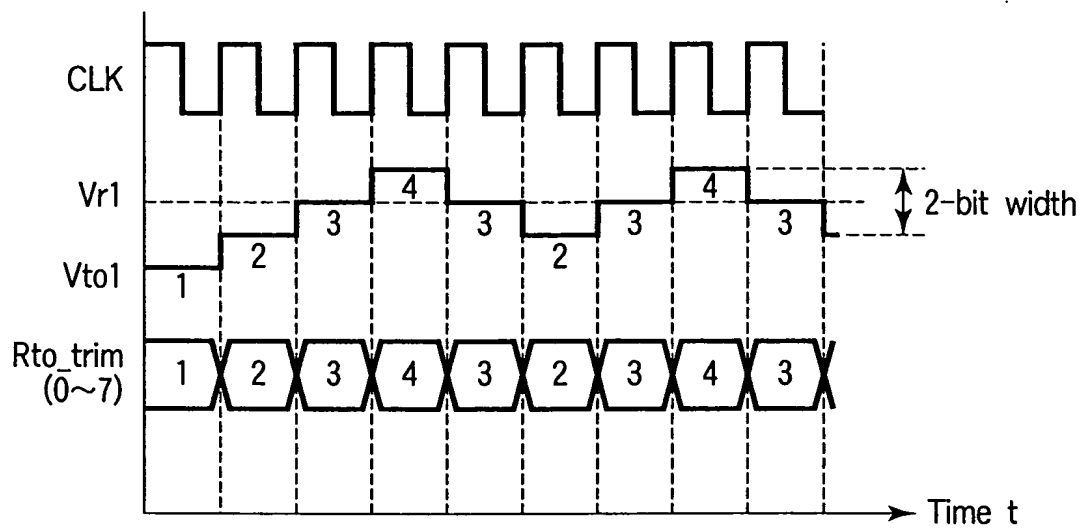


FIG. 18

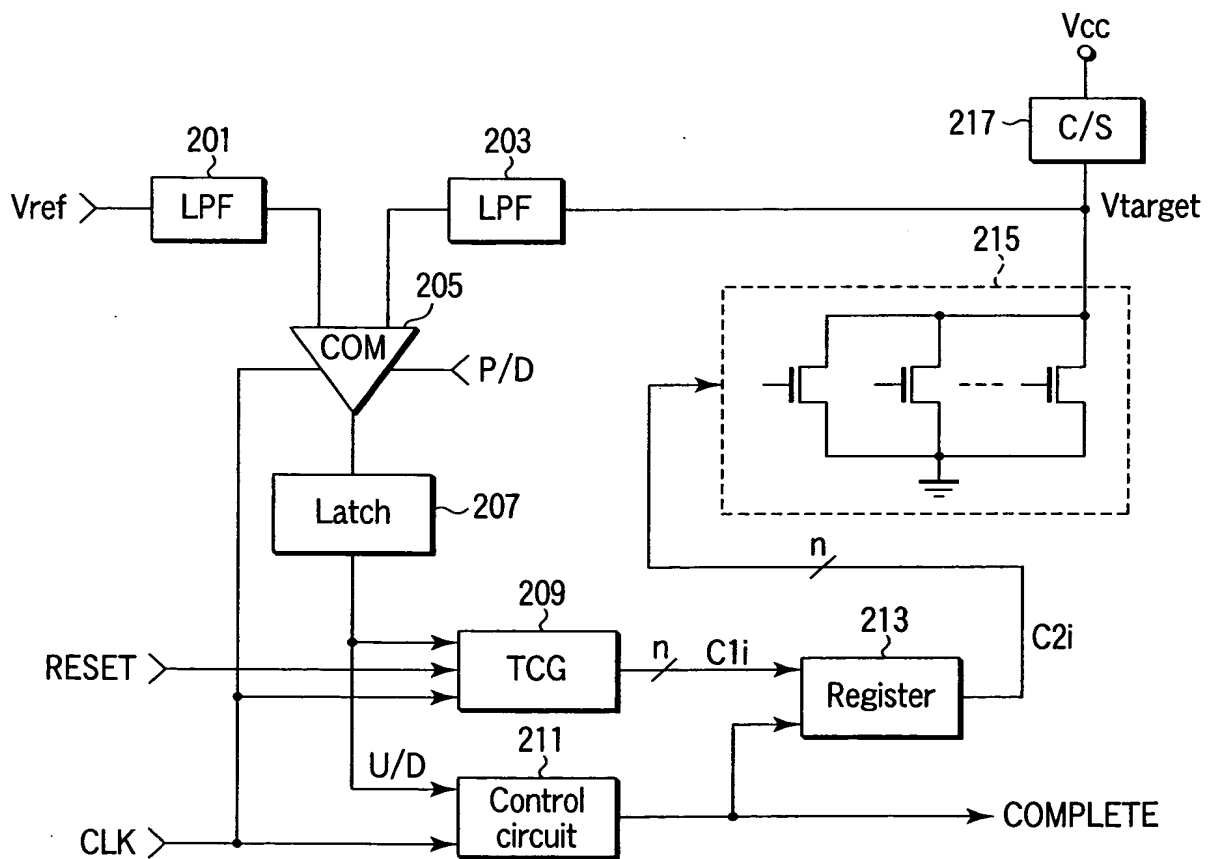


FIG. 19

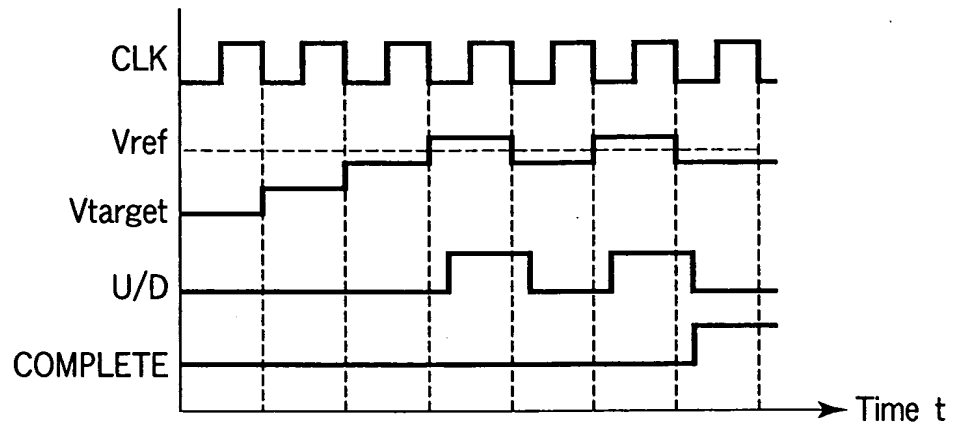


FIG. 20

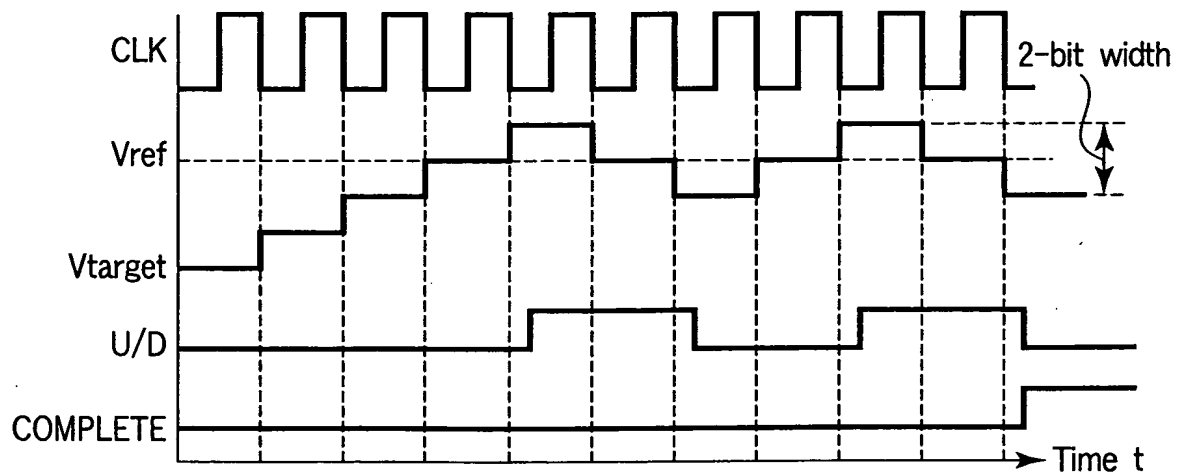


FIG. 21

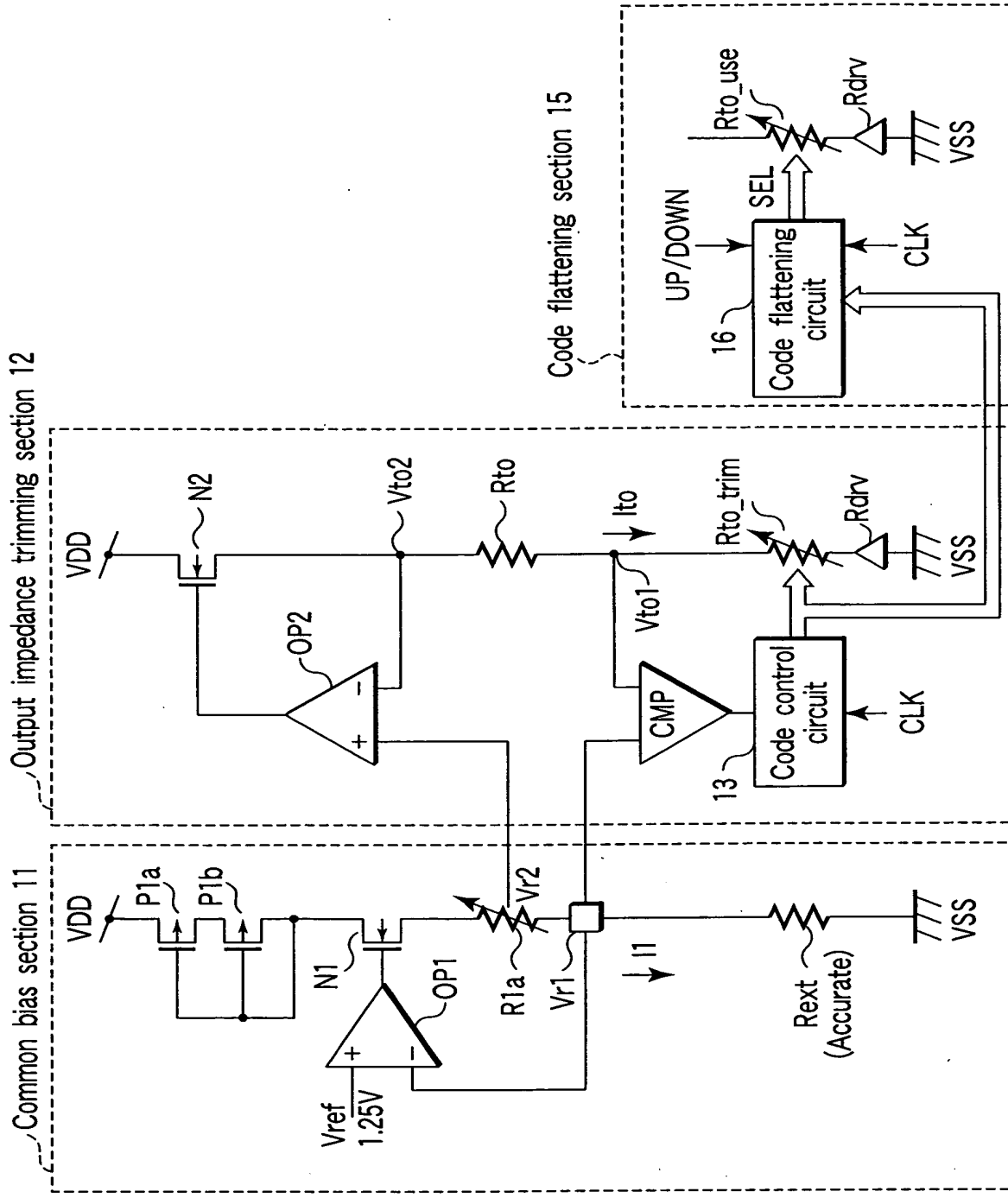


FIG. 22

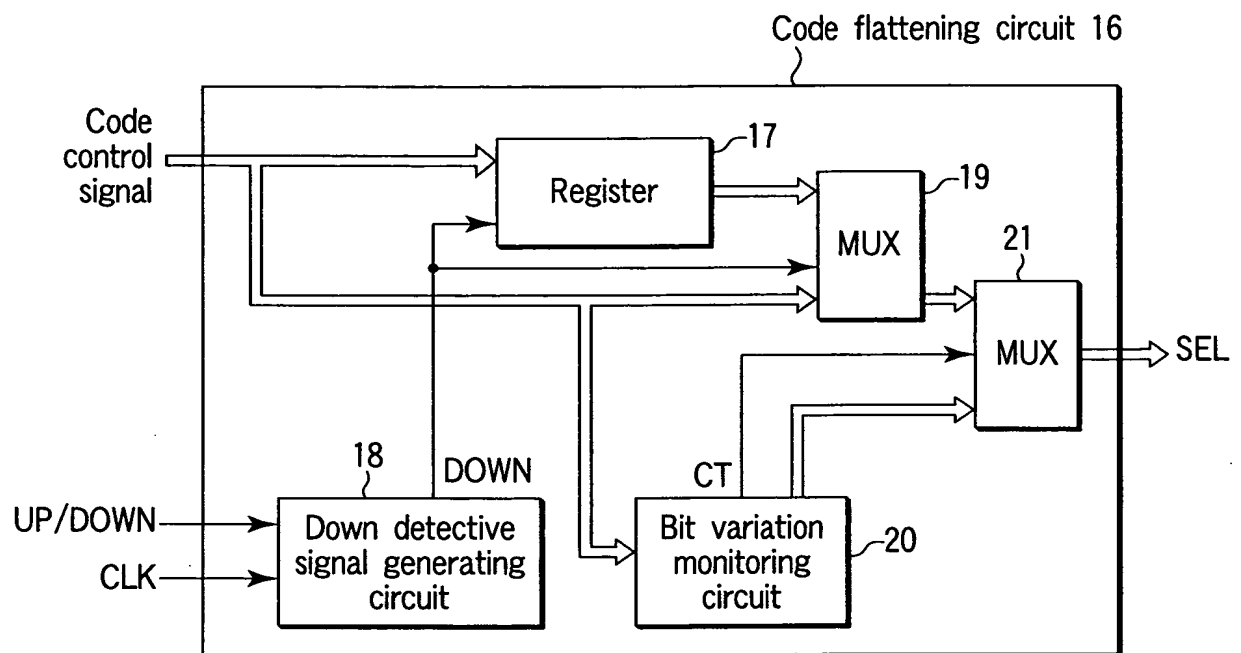


FIG. 23

When Vto1 varies by 1-bit width

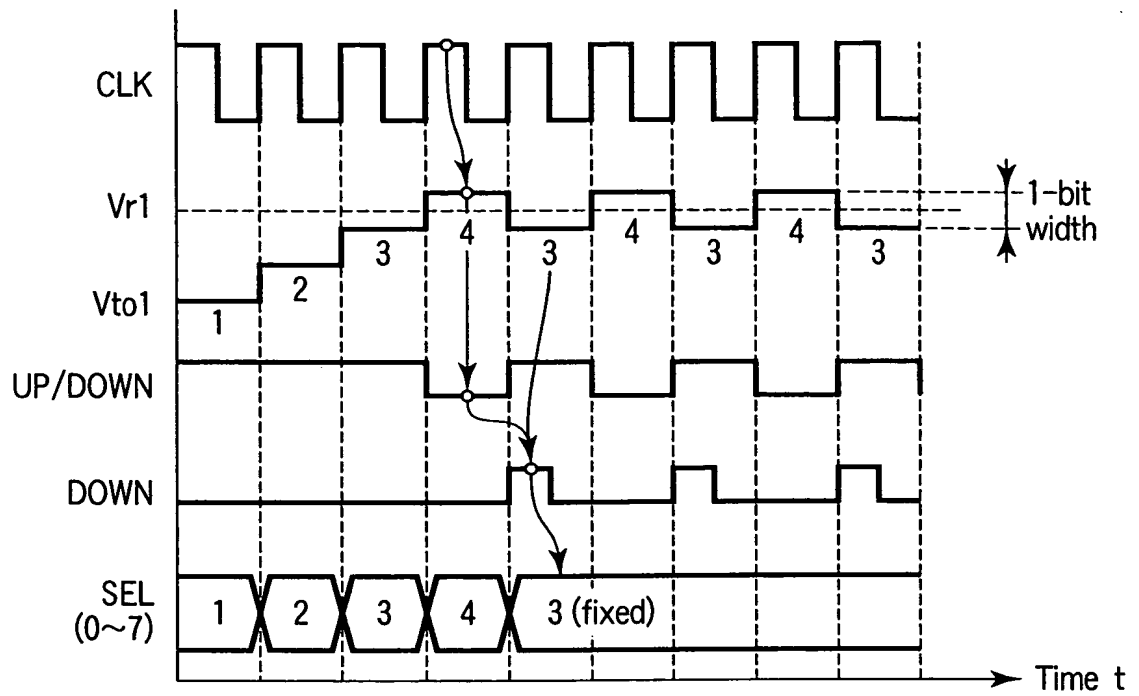


FIG. 24

When Vto1 varies by 2-bit width

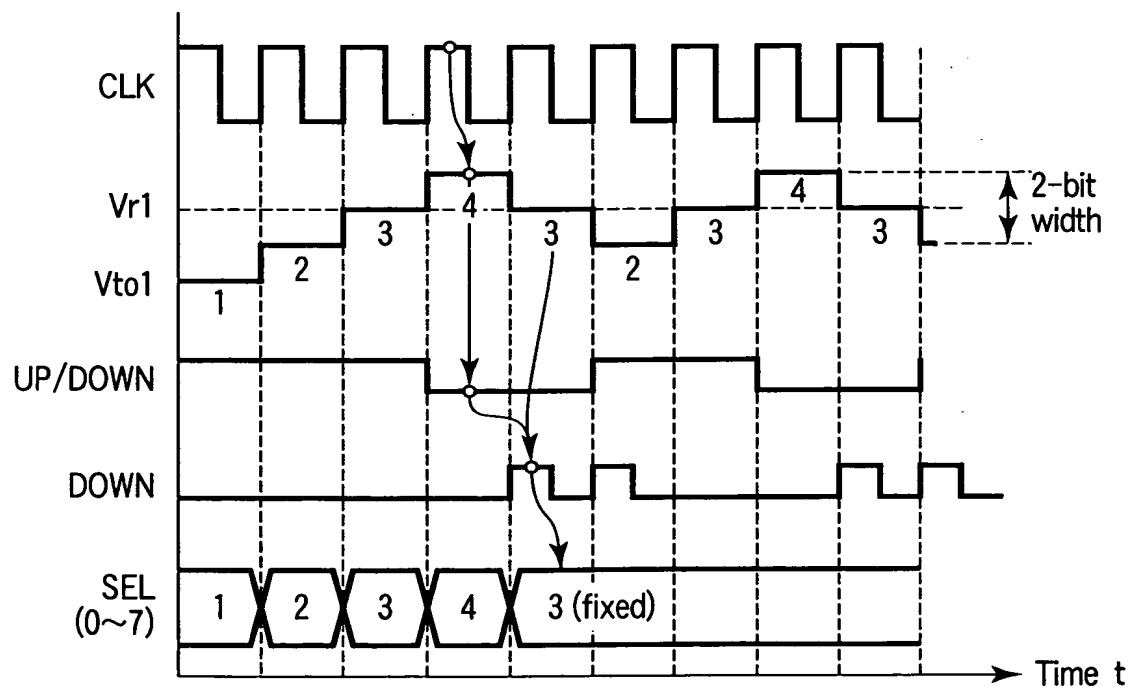


FIG. 25

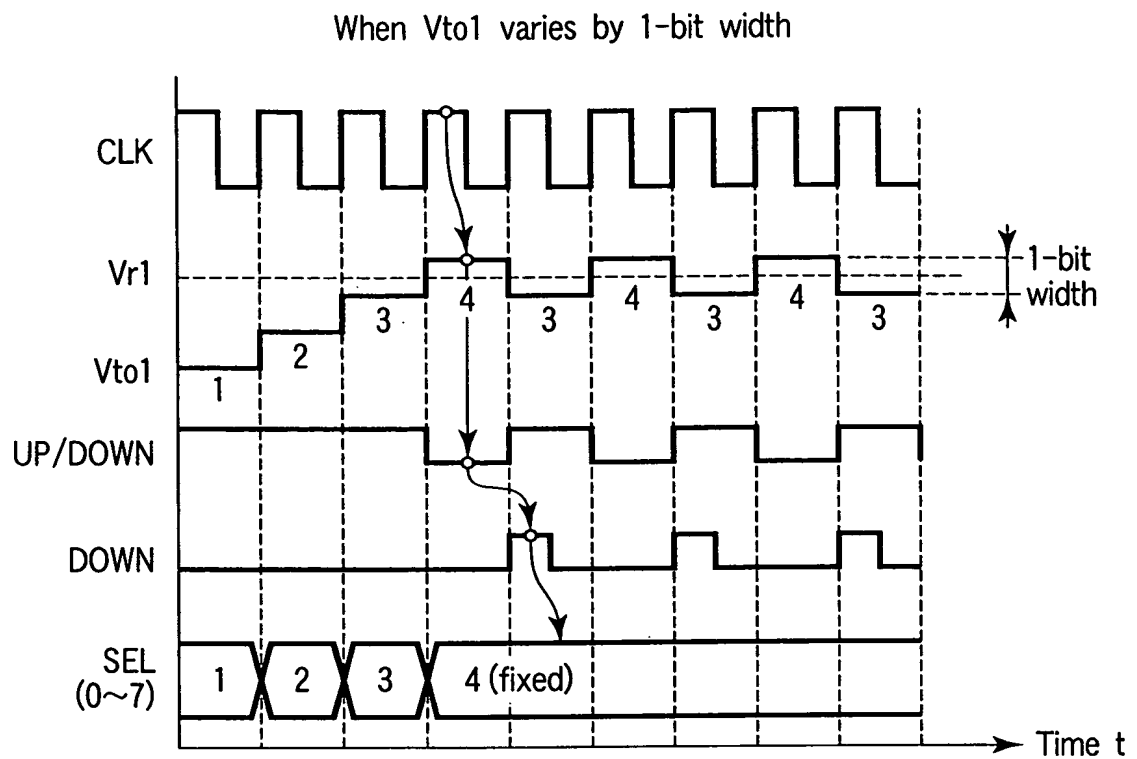


FIG. 26

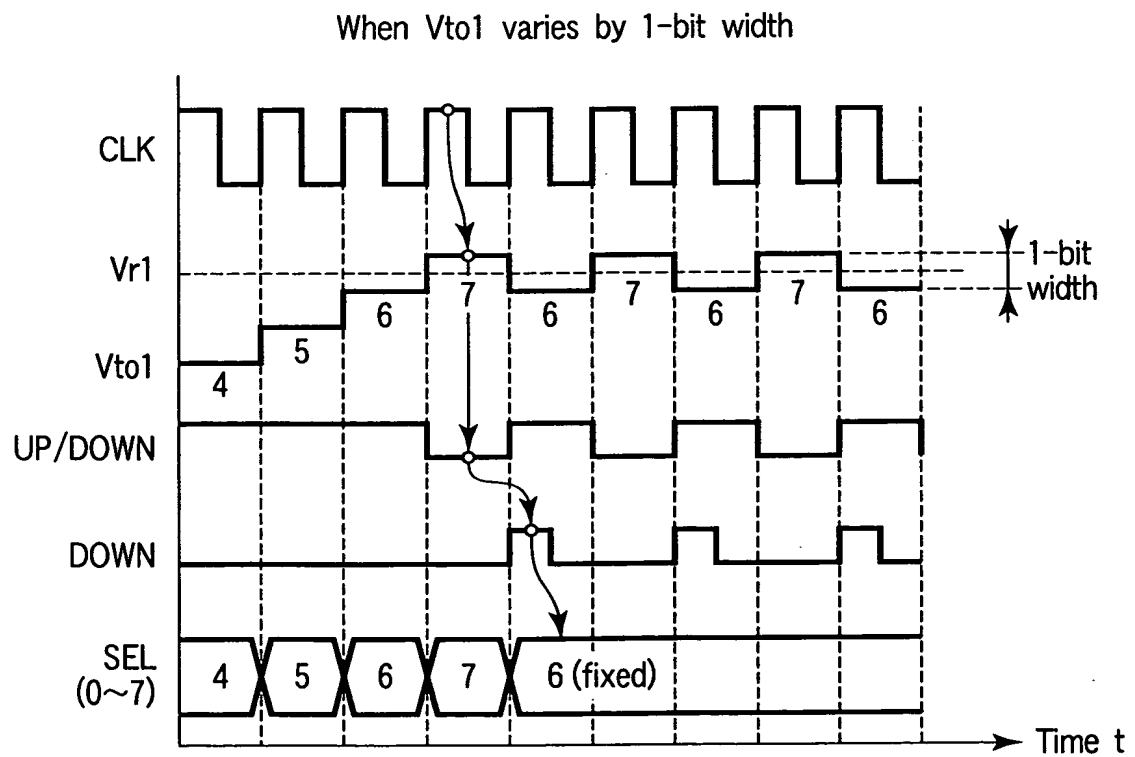


FIG. 27

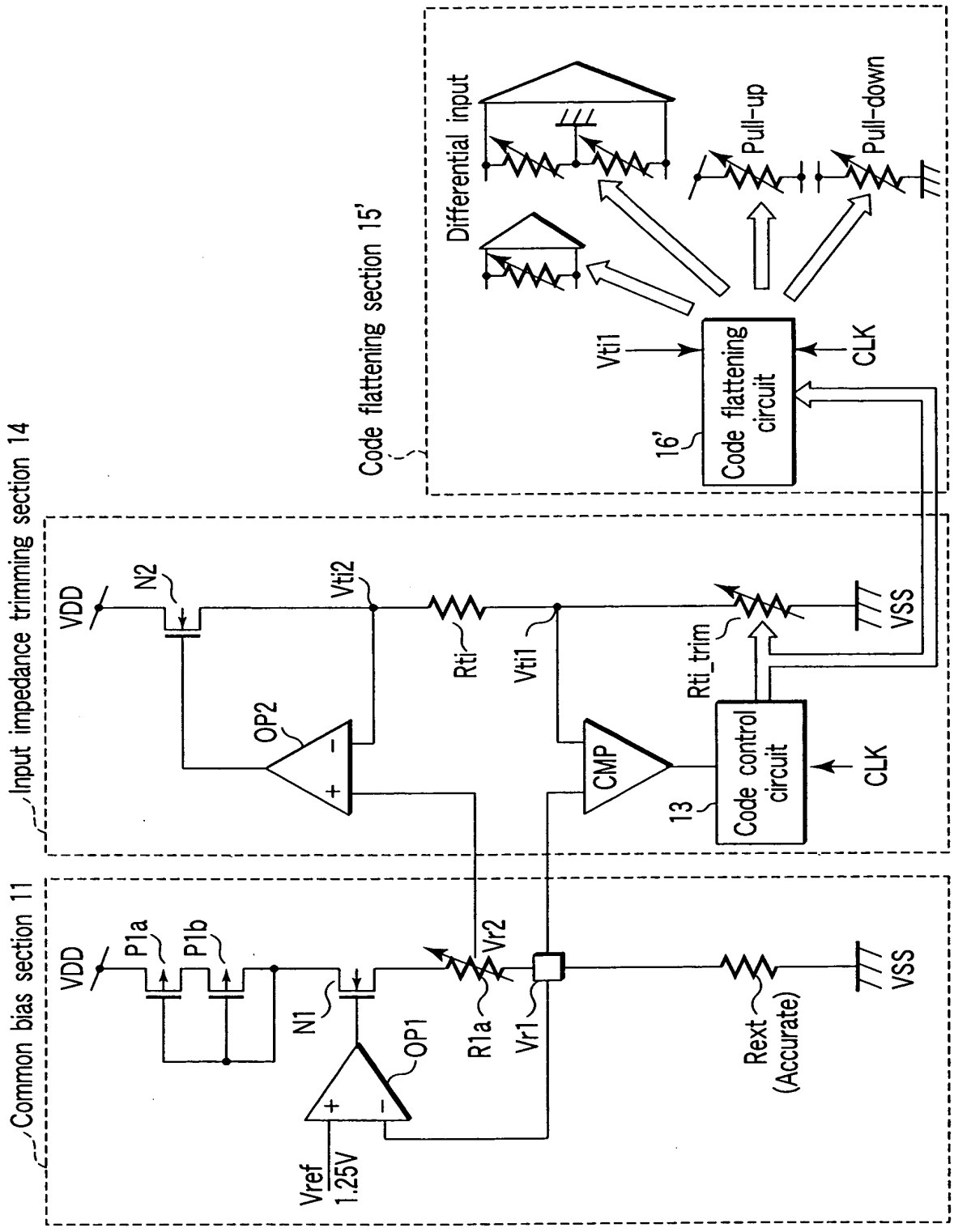


FIG. 28



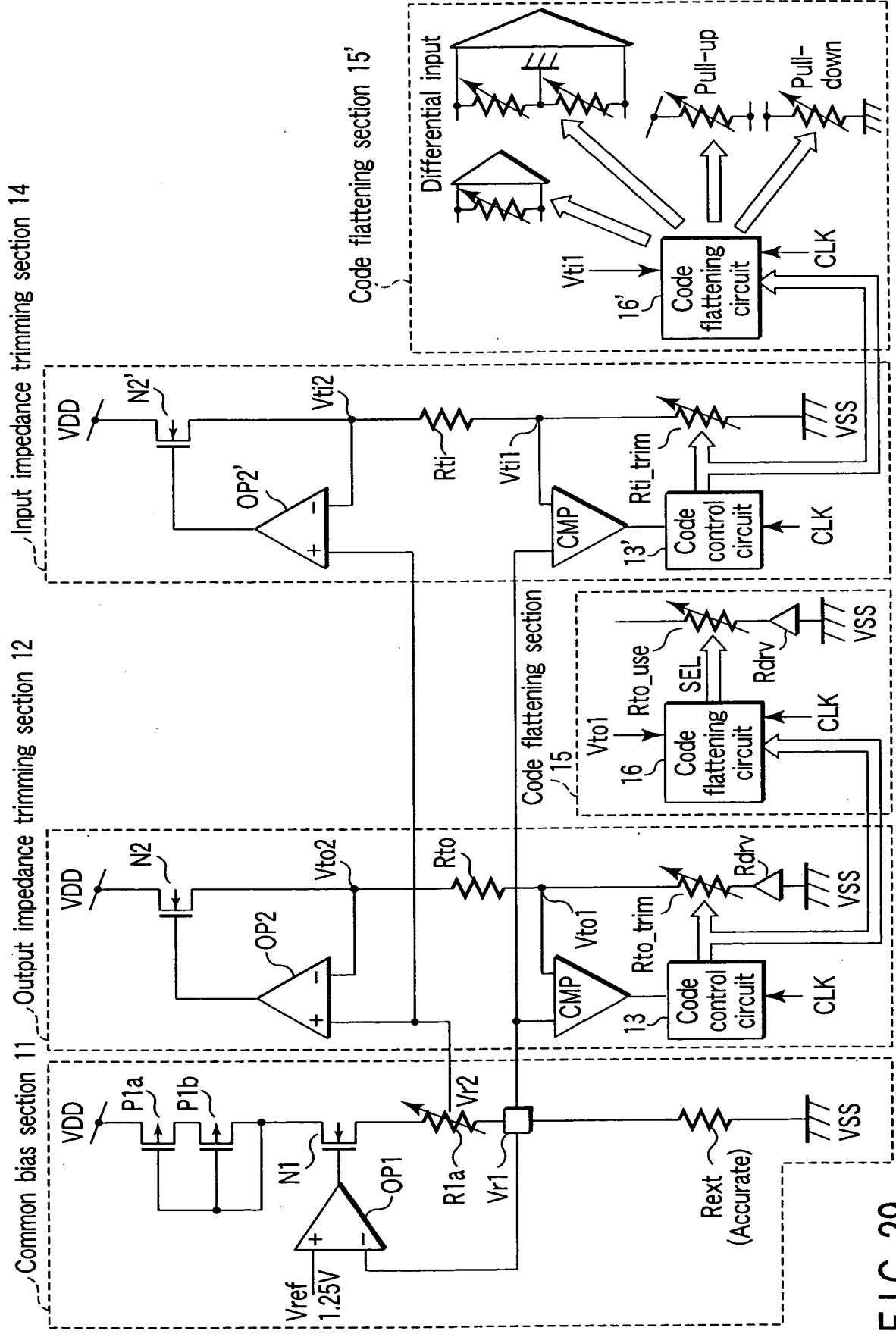


FIG. 29

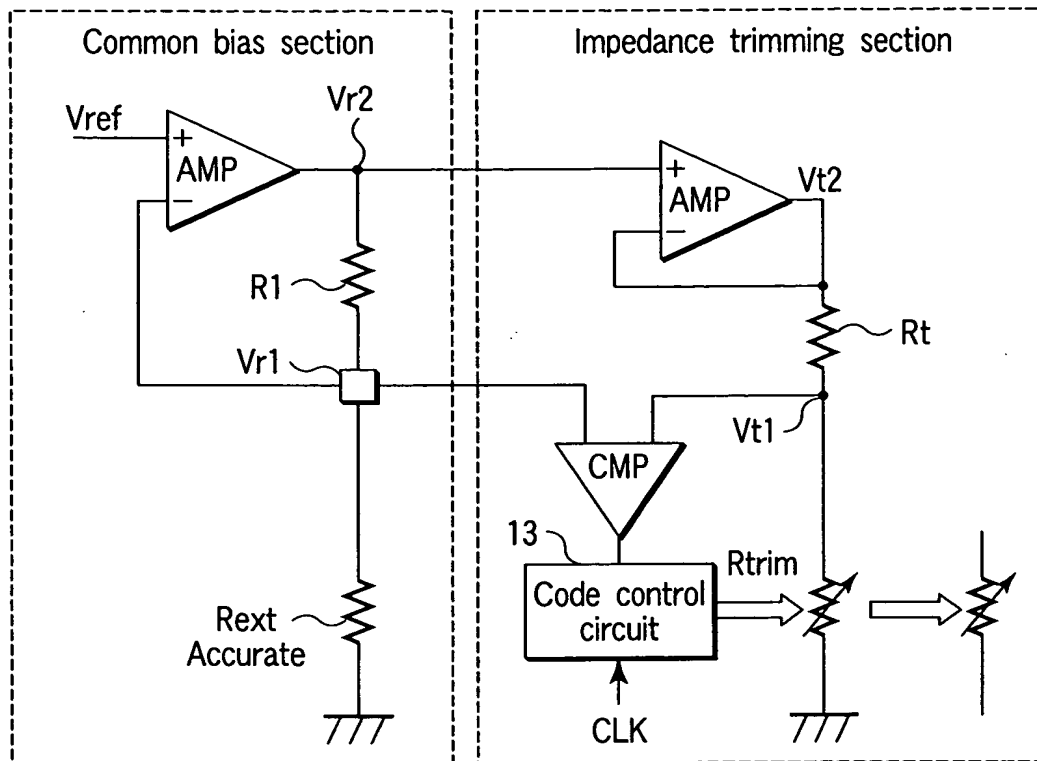


FIG. 30

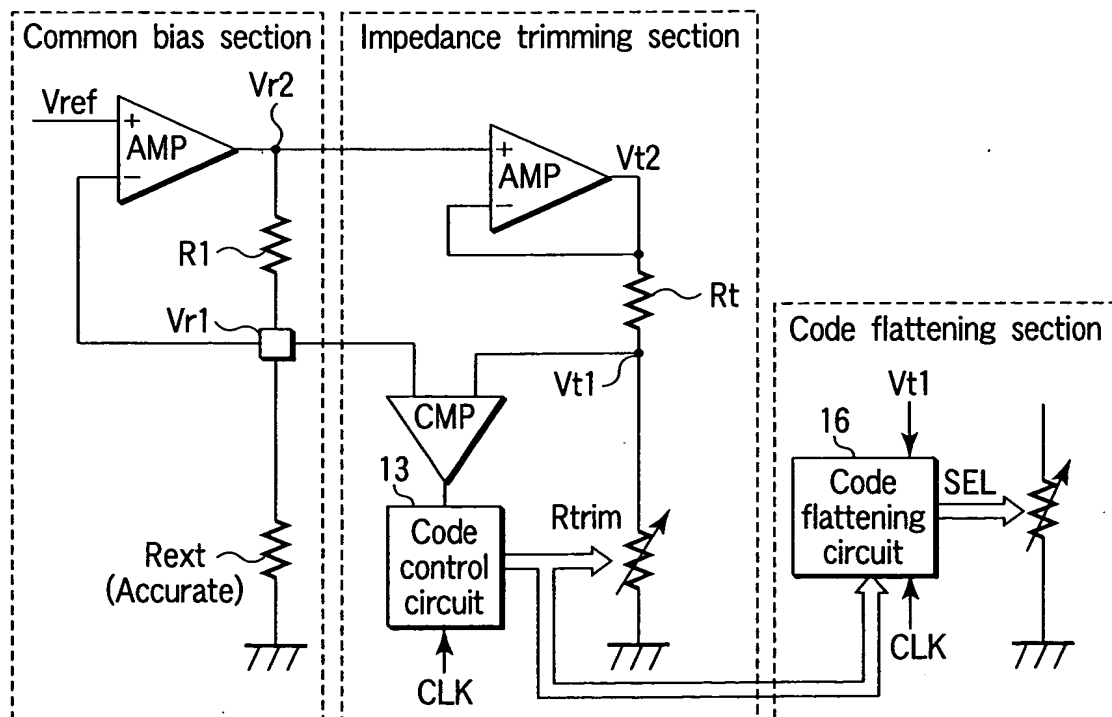


FIG. 31